

INVENTOR INFORMATION

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APPLICATION INFORMATION

Title Line One:: MULTILAYER CIRCUIT BOARD, MANUFACTURING
Title Line Two:: METHOD THEREFOR, ELECTRONIC DEVICE, AND
Title Line Three:: ELECTRONIC APPARATUS
Total Drawing Sheets:: 13
Formal Drawings?: Yes
Application Type:: Utility
Docket Number:: 9319G-000593
Secrecy Order in Parent Appl.?: No

PRIOR FOREIGN APPLICATIONS

Foreign Application One:: 2002-334915
Filing Date:: 11-19-2002
Country:: Japan
Priority Claimed:: Yes
Foreign Application Two:: 2003-300143
Filing Date:: 08-25-2003
Country:: Japan
Priority Claimed:: Yes

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